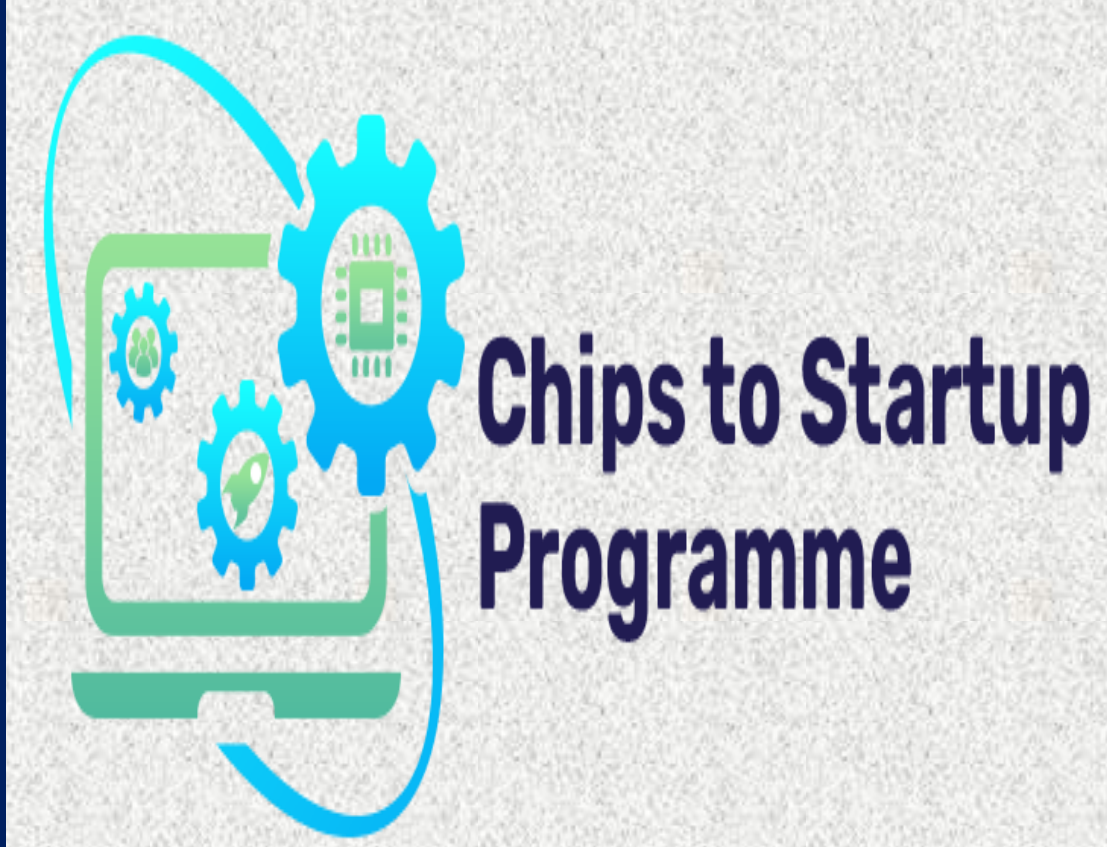




VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY

Workshop on "Trends in VLSI Design with VERILOG Programming"

Under the Umbrella programme "Chips to Startup(C2S)"



IQAC - VESIT



Ministry of Electronics & Information Technology (MeitY)

ABOUT INSTITUTE :

Vivekanand Education Society's Institute of Technology(VESIT) established in 1984, is one of the premier engineering colleges affiliated with the University of Mumbai. VESIT offers

Bachelors programs in Engineering :

1. Computer Engineering
2. Information Technology
3. Artificial Intelligence & Data Science
4. Electronics & Computer Science
5. Electronics and Telecommunication
6. Automation and Robotics

Post Graduate Programs in

1. **VLSI and Embedded system(New course)**
2. Information and Technology
3. Master of Computer Application.

VESIT is recognized Ph.D. Center of Engineering and Technology of the University of Mumbai, and also a Lifelong Learning Center.

ABOUT CHIP TO STARTUP (C2S) :

1. In line with the objective and vision of NPE-2019, an umbrella programme "Chips to Startup(C2S)" has been started. The purpose is to *develop human resource in the field of semiconductor and VLSI*.
2. Under Industry-Academia Collaborative Projects V.E.S. Institute of Technology, Mumbai and M/s Panache Digilife Limited, Thane's research project entitled "Development of ASICs or SOCs for programmable gain amplifier and reconfigurable ADC for wide range of applications," received a total estimated amount of Rs. 82.59 Lakh, Rs.74.32 Lakh from the Ministry of Electronics and Information Technology as Grant-in-Aid, and Rs.8.27 Lakh from M/s Panache Digilife Limited, Thane.
3. The duration of the Project is 3 years. The EDA Tools (Remote Access of Licences - Mentor, Cadence, Synopsys, Xilinx Vivado) will be issued for the purpose of undergraduate and postgraduate students' projects.

About Workshop : The Vision of India Semiconductor Mission (ISM) is to build a vibrant semiconductor design and innovation ecosystem aims at developing Specialized Manpower in VLSI/Embedded System Design. VERILOG Programming and FPGA prototyping is very popular trend in VLSI.

Objectives :

1. Learn Basic VLSI design flow using Xilinx Tool
2. Train Students for Verilog programming
3. Implement and Test designs on FPGA and interface peripherals
4. Introduce opportunities in VLSI and IC design

Outcomes : After successfully completing the workshop participants can

1. Implement Programs in VERILOG using Xilinx tool
2. Implement programs on FPGA boards
3. Use different interfaces of FPGA boards

Registration Information of Workshop

Duration : (1 Week)

10th July- 2023 – 14th July 2023

Fee : Free (Rs. 200/- for certificate)

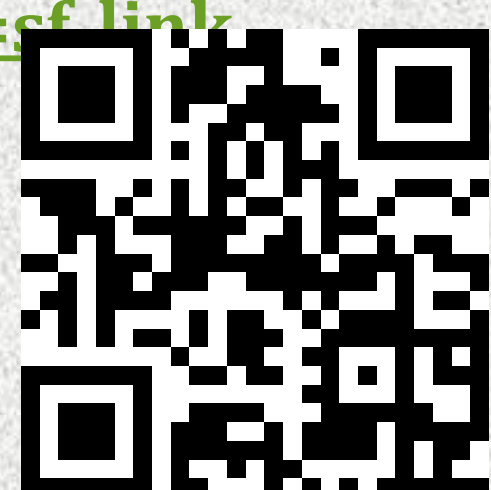
Mode : Offline

BE passed students only

1. Laptop is Mandatory for Hands-on sessions
2. Limited Seats -First Come First Serve

Link for Registration :

https://docs.google.com/forms/d/e/1FAIpQLSfY_C6aPWVQKk75RuN0dZ4NMnfPDX6oAPhV1Bkj--CX-WPV9g/viewform?usp=cf_entry



Contents	Resource Person
<ul style="list-style-type: none"> • Overview of VERILOG, Data Types, Modelling Techniques using Xilinx Tool • Implementation of 1-Bit adder, 4-Bit adder, n-Bit adder and downloading on FPGA. • Writing Test bench. Interfacing LED & 7-segment display with FPGA. • Implementation of MUX, Decoder, 	Prof. Mrugendra Vasmatkar, Assistant Professor, VESIT
Introduction to Schematic Entry using Cadence	Dr. Jayamala Adsul Assistant Professor, ECS
Seminar on – Future of IC design in India	Industry Talk
Opportunities and trends in FPGA design	Ms. Sonali Dulange AMD-Xilinx
Seminar on – Drone development using FPGA	Mr.Anirudha Pradhan Aten Aerospace and automation

PATRONS

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Managing Trustee, VESIT

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Principal, VESIT

Dr.(Mrs) M. Vijayalakshmi
Vice-Principal, VESIT

Dr. P.P. Vaidya
Honorable Dean R&D

CONVENORS

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Associate Professor,
Automation & Robotics

Dr. Jayamala Adsul
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Electronics & Computer
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VENUE :

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